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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/408,430	
	Filing Date	September 29, 1999	
	First Named Inventor	Kendell A. Chilton	
	Group Art Unit	2751	
	Examiner Name	Not Yet Assigned	
Total Number of Pages in This Submission	5	Attorney Docket Number	EMC2-033PUS

ENCLOSURES (check all that apply)		
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Date	March 19, 2002

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#4/A
L25
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entered

Applicant(s): Kendell A. Chilton

Serial No.: 09/408,430

Filed: September 29, 1999

Entitled: Interrupt Request Controller

Docket No.: EMC2-033PUS (formerly 07072-087001)

Group Art Unit: 2751

Examiner: Not Yet Assigned

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SUPPLEMENTAL AMENDMENT

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Please amend the above-identified application as follows:

Technology Center 2100

In The Specification:

Please replace the paragraph beginning on page 27 at line 20 and continuing to line 33
with the following (a comparison copy with the original text is attached):

Referring now to FIG. 9, a circuit 400 is shown for detecting a hard fault (i.e., a ground fault
or a connection to a fixed, unchanging voltage) on a bi-directional data line 402. The circuit 400

includes an input/output terminal 304 connected to one end of the bi-directional line 402; a data receiver 406 having an input coupled to a second end of the bi-directional line 402 for receiving data on the terminal 404; an data driver 408 having an output coupled to second end of the bi-directional line 402 for producing data on the terminal 404; an XOR gate 410 having a pair of inputs, one being coupled to an input of the data driver 408 and the other being coupled to an output of the data receiver 408, for determining whether data produced by the data driver is received by the data receiver.

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end